



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani, et al. Art Unit : 2815
Serial No.: 09/379,702 Examiner : Eugene Lee
Filed : August 24, 1999
Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICES

Commissioner for Patents
Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges
not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	43	43	=	0	\$0
Independent	10	10	=	0	\$0

Applicant hereby petitions under 37 C.F.R.
\$1.136 for a 2 month extension of time. \$390

TOTAL FEE DUE \$390

A check for \$390 is attached.

Respectfully submitted,

Date: 4/12/01


Scott C. Harris
Reg. No. 32,030

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

April 12, 2001

Date of Deposit



Signature

Susan Regan

Typed or Printed Name of Person Signing Certificate